

Atty. Docket No. OPP031475US  
Serial No: 10/748,652

Amendments to the Claims

Please amend claims 1, 2, 4, 5 and 9 and add new claims 10 – 12 as follows:

1. (Currently Amended) A method for forming a trench in a semiconductor device comprising:
  - forming a pad oxide film and a silicon nitride film on a ~~semiconductor-substrate~~ silicon wafer;
  - forming a first photosensitive film pattern on the region to be formed with the trench by applying, exposing, and developing a photosensitive film on the silicon nitride film;
  - selectively etching the silicon nitride film and the pad oxide film on a region to be formed with a trench using the first photosensitive film pattern as a mask;
  - forming a second photosensitive film pattern having an opening portion of a width narrower than that of an opening portion of the first photosensitive film pattern on the silicon nitride film and the exposed silicon wafer, the opening portion of the second photosensitive film pattern having a center axis as the opening portion of the first photosensitive film pattern;
  - implanting oxygen ions into the ~~semiconductor-substrate~~ silicon wafer in the region to be formed with the trench at a desired depth of the trench using the second photosensitive film pattern as a mask;
  - forming an oxide in the ~~semiconductor-substrate~~ silicon wafer by reacting the oxygen ions with the ~~semiconductor-substrate~~ silicon wafer through a thermal diffusion of the oxygen ions;
  - forming the trench by etching the ~~semiconductor-substrate~~ silicon wafer and the oxide on the region to be formed with the trench using the silicon nitride film as a mask;
  - forming a liner oxide film on an inner wall of the trench using a thermal diffusion process; and
  - forming an insulation film on the liner oxide film such that the trench is filled, wherein an edge at which a side and a bottom of the trench intersect has a curved surface.

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2. (Currently Amended) The method of claim ~~[[1]]~~ 10, wherein the semiconductor substrate is a silicon wafer.

3. (Original) The method of claim 2, wherein, in selectively etching the silicon nitride film and the pad oxide film, a first photosensitive film pattern for exposing the silicon nitride film on the region to be formed with the trench is formed by applying, exposing, and developing a photosensitive film on the silicon nitride film, and then the silicon nitride film and the pad oxide film exposed are selectively etched using the first photosensitive film pattern as a mask.

4. (Currently Amended) The method of claim 3, wherein, in implanting the oxygen ions, a second photosensitive film pattern having an opening portion of a width narrower than that of an opening portion of the first photosensitive film pattern is formed on the silicon nitride film and the exposed ~~semiconductor-substrate~~ silicon wafer, the opening portion of the second photosensitive film pattern having the center axis as the opening portion of the first photosensitive film pattern, the oxygen ions are implanted using the second photosensitive film pattern as a mask, and wherein the oxygen ions are implanted into the semiconductor substrate at a desired depth of the trench.

5. (Currently Amended) The method of claim ~~[[4]]~~ 1, wherein, during thermal diffusion of the oxygen ions, the oxygen ions are thermally diffused in a nitrogen atmosphere.

6. (Cancelled)

7. (Original) The method of claim 1, wherein, in forming the insulation film on the liner oxide film such that the trench is filled, an oxide film is formed on an entire top surface including the silicon nitride film and the trench such that the trench is filled, and then the oxide film is chemically and mechanically polished until the silicon nitride film is exposed.

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8. (Original) The method of claim 7, wherein the silicon nitride film is deposited at a thickness of 1,000 – 3,000 Å.

9. (Currently Amended) The method of claim 1, wherein said step of forming the trench comprises etching the ~~semiconductor-substrate~~ silicon wafer and the oxide on the region to be formed with the trench at different rates.

10. (New) A method for forming a trench in a semiconductor device comprising:  
forming a pad oxide film and a silicon nitride film on a semiconductor substrate;  
selectively etching the silicon nitride film and the pad oxide film on a region to be formed with a trench;  
implanting oxygen ions into the semiconductor substrate in the region to be formed with the trench;  
forming an oxide in the semiconductor substrate by reacting the oxygen ions with the semiconductor substrate through a thermal diffusion of the oxygen ions;  
forming the trench by etching the semiconductor substrate and the oxide on the region to be formed with the trench using the silicon nitride film as a mask, wherein an edge at which a side and a bottom of the trench intersect has a curved surface;  
forming a liner oxide film on an inner wall of the trench using a thermal diffusion process; and  
forming an oxide film on an entire top surface including the liner oxide film and the silicon nitride film such that the trench is filled, then chemically and mechanically polishing the oxide film until the silicon nitride film is exposed.

11. (New) The method of claim 10, wherein the silicon nitride film is deposited at a thickness of 1,000 – 3,000 Å.

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12. (New) The method of claim 10, wherein said step of forming the trench comprises etching the semiconductor substrate and the oxide on the region to be formed with the trench at different rates.